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KEY=EDITION - GRIMES RODNEY

Embedded SoPC Design with Nios II Processor and VHDL Examples

John Wiley & Sons The book is divided into four major parts. Part I covers HDL constructs and synthesis of basic digital circuits. Part II provides an overview of embedded software development with the emphasis on low-level I/O access and drivers. Part III demonstrates the design and development of hardware and software for several complex I/O peripherals, including PS2 keyboard and mouse, a graphic video controller, an audio codec, and an SD (secure digital) card. Part IV provides three case studies of the integration of hardware accelerators, including a custom GCD (greatest common divisor) circuit, a Mandelbrot set fractal circuit, and an audio synthesizer based on DDFS (direct digital frequency synthesis) methodology. The book utilizes FPGA devices, Nios II soft-core processor, and development platform from Altera Co., which is one of the two main FPGA manufactures. Altera has a generous university program that provides free software and discounted prototyping boards for educational institutions (details at <http://www.altera.com/university>). The two main educational prototyping boards are known as DE1 (\$99) and DE2 (\$269). All experiments can be implemented and tested with these boards. A board combined with this book becomes a "turn-key" solution for the SoPC design experiments and projects. Most HDL and C codes in the book are device independent and can be adapted by other prototyping boards as long as a board has similar I/O configuration.

Embedded SoPC Design with Nios II Processor and Verilog Examples

John Wiley & Sons Explores the unique hardware programmability of FPGA-based embedded systems, using a learn-by-doing approach to introduce the concepts and techniques for embedded SoPC design with Verilog. An SoPC (system on a programmable chip) integrates a processor, memory modules, I/O peripherals, and custom hardware accelerators into a single FPGA (field-programmable gate array) device. In addition to the customized software, customized hardware can be developed and incorporated into the embedded system as well—allowing us to configure the soft-core processor, create tailored I/O interfaces, and develop specialized hardware accelerators for computation-intensive tasks. Utilizing an Altera FPGA prototyping board and its Nios II soft-core processor, Embedded SoPC Design with Nios II Processor and Verilog Examples takes a "learn by doing" approach to illustrate the hardware and software design and development process by including realistic projects that can be implemented and tested on the board. Emphasizing hardware design and integration throughout, the book is divided into four major parts: Part I covers HDL and synthesis of custom hardware. Part II introduces the Nios II processor and provides an overview of embedded software development. Part III demonstrates the design and development of hardware and software of several complex I/O peripherals, including a PS2 keyboard and mouse, a graphic video controller, an audio codec, and an SD (secure digital) card. Part IV provides several case studies of the integration of hardware accelerators, including a custom GCD (greatest common divisor) circuit, a Mandelbrot set fractal circuit, and an audio synthesizer based on DDFS (direct digital frequency synthesis) methodology. While designing and developing an embedded SoPC can be rewarding, the learning can be a long and winding journey. This book shows the trail ahead and guides readers through the initial steps to exploit the full potential of this emerging methodology.

Introduction to Logic Design

CRC Press The second edition of this text provides an introduction to the analysis and design of digital circuits at a logic, instead of electronics, level. It covers a range of topics, from number system theory to asynchronous logic design. A solution manual is available to instructors only. Requests must be made on official school stationery.

FPGA Prototyping by VHDL Examples

Xilinx Spartan-3 Version

John Wiley & Sons This book uses a "learn by doing" approach to introduce the concepts and techniques of VHDL and FPGA to designers through a series of hands-on experiments. FPGA Prototyping by VHDL Examples provides a collection of clear, easy-to-follow templates for quick code development; a large number of practical examples to illustrate and reinforce the concepts and design techniques; realistic projects that can be implemented and tested on a Xilinx prototyping board; and a thorough exploration of the Xilinx PicoBlaze soft-core microcontroller.

Publishers' Trade List Annual

Department of Defense Catalog of Logistics Models

Catalog of Copyright Entries. Third Series

1977: January-June

Copyright Office, Library of Congress

Handbook of Fuzzy Computation

CRC Press Initially conceived as a methodology for the representation and manipulation of imprecise and vague information, fuzzy computation has found wide use in problems that fall well beyond its originally intended scope of application. Many scientists and engineers now use the paradigms of fuzzy computation to tackle problems that are either intractable

Engineering Education

150 technical questions and answers for job interview Offshore Drilling Rigs

Petrogav International The job interview is probably the most important step you will take in your job search journey. Because it's always important to be prepared to respond effectively to the questions that employers typically ask at a job interview Petrogav International has prepared this eBooks that will help you to get a job in oil and gas industry. Since these questions are so common, hiring managers will expect you to be able to answer them smoothly and without hesitation. This eBook contains 150 questions and answers for job interview and as a BONUS 230 links to video movies. This course covers aspects like HSE, Process, Mechanical, Electrical and Instrumentation & Control that will enable you to apply for any position in the Oil and Gas Industry.

273 technical questions and answers for job interview Offshore Drilling Rigs

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Scientific and Technical Books and Serials in Print

Questions and answers for job interview Offshore Oil & Gas Platforms

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Technical questions and answers for job interview Offshore Drilling Platforms

Petrogav International This book offers you a brief, but very involved look into the operations in the drilling of an oil & gas wells that will help you to be prepared for job interview at oil & gas companies. From start to finish, you'll see a general prognosis of the drilling process. If you are new to the oil & gas industry, you'll enjoy having a leg up with the knowledge of these processes. If you are a seasoned oil & gas person, you'll enjoy reading what you may or may not know in these pages. This course provides a non-technical overview of the phases, operations and terminology used on offshore drilling platforms. It is intended also for non-drilling personnel who work in the offshore drilling, exploration and production industry. This includes marine and logistics personnel, accounting, administrative and support staff, environmental professionals, etc. No prior experience or knowledge of drilling operations is required. This course will provide participants a better understanding of the issues faced in all aspects of drilling operations, with a particular focus on the unique aspects of offshore operations.

Technical questions and answers for job interview Offshore Oil & Gas Platforms

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VHDL Answers to Frequently Asked Questions

Springer Science & Business Media VHDL Answers to Frequently asked Questions is a follow-up to the author's book VHDL Coding Styles and Methodologies (ISBN 0-7923-9598-0). On completion of his first book, the author continued teaching VHDL and actively participated in the comp. lang. vhdl newsgroup. During his experiences, he was enlightened by the many interesting issues and questions relating to VHDL and synthesis. These pertained to: misinterpretations in the use of the language; methods for writing error free, and simulation efficient, code for testbench designs and for synthesis; and general principles and guidelines for design verification. As a result of this wealth of public knowledge contributed by a large VHDL community, the author decided to act as a facilitator of this information by collecting different classes of VHDL issues, and by elaborating on these topics through complete simulatable examples. This book is intended for those who are seeking an enhanced proficiency in VHDL. Its target audience includes: 1. Engineers. The book addresses a set of problems commonly experienced by real users of VHDL. It provides practical explanations to the questions, and suggests practical solutions to the raised issues. It also includes packages of common utilities that are useful in the generation of debug code and testbench designs. These packages include conversions to strings (the IMAGE package), generation of Linear Feedback Shift Registers (LFSR), Multiple Input Shift Register (MISR), and random number generators.

Books in Print Supplement

100 technical questions and answers for job interview Offshore Drilling Rigs

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The Publishers' Trade List Annual

Instructors Resource Manual with Solutions and Test Item File

Prentice Hall

Subject Guide to Books in Print

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Univ. Santiago de Compostela

Monthly Catalog of United States Government Publications

Cumulative index

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Monthly Catalog of United States Government Publications, Cumulative Index

Index to the Monthly Issues

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Monthly Catalog of United States Government Publications

February issue includes Appendix entitled Directory of United States Government periodicals and subscription publications; September issue includes List of depository libraries; June and December issues include semiannual index

Radio-electronics

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Electronic Musician

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Optoelectronics Guidebook

With Tested Projects

Manual of Logic Circuits

Prentice Hall